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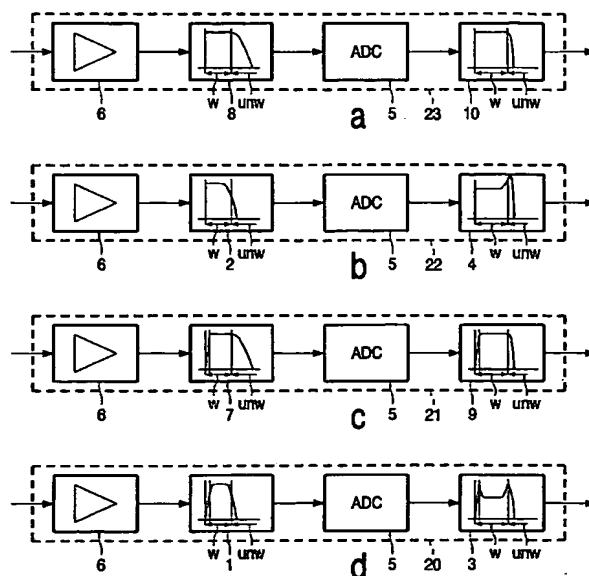
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- Declaration under Rule 4.17:**
— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ,

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- (54) Title: ANALOG-TO-DIGITAL-CONVERTING AND SUPPRESSING SYSTEM



- (57) Abstract:** Systems (20, 22) with analog-to-digital-converters (5), for use in receivers (30) comprising tuners (31) and channel decoders (32), comprise analog filters (1, 2) located before the analog-to-digital-converters (5). The analog filters (1, 2) at least partly suppress the analog wanted signal and the analog unwanted signal. Compensators (3, 4) compensate the digital output signal of the analog-to-digital-converters (5) for the at least partly suppressing of the analog wanted signal. As a result, the analog unwanted signal in an adjacent channel could now be suppressed better than before, at the cost of also partly suppressing the analog wanted signal in a wanted channel. The compensators (3, 4) restore the wanted signal.



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